

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Hui ZHONG, et al.

Appln. No.: 10/049,270

Confirmation No.: 9445

Filed: February 11, 2002

Docket No: Q68281

Group Art Unit: 2811

Examiner: Not yet designated

September 18, 2002



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For: MULTILAYERED PRINTED CIRCUIT BOARD, SOLDER RESIST COMPOSITION,  
MULTILAYERED PRINTED CIRCUIT BOARD MANUFACTURING METHOD, AND  
SEMICONDUCTOR DEVICE

**REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT**

Commissioner for Patents  
Office of Initial Patent Examination  
Customer Service Center  
Washington, D.C. 20231

Sir:

We enclose a copy of the Official Filing Receipt for the above-identified application and  
request the following corrections:

**Title should read:**

**Multilayered printed circuit board, solder resist composition, multilayered printed  
circuit board manufacturing method, and semiconductor device**

Verification for the requested corrections is indicated on the original Declaration and  
Power of Attorney filed June 27, 2002.

Respectfully submitted,

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Date: September 18, 2002



## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/049,270	06/27/2002	2811	2204	Q68281	36	33	9

CONFIRMATION NO. 9445 ✓

23373  
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## FILING RECEIPT



\*OC000000008531235\*

Date Mailed: 08/08/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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## Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/JP00/05044 07/28/2000 ✓

## Foreign Applications

JAPAN 11/228852 08/12/1999 ✓  
JAPAN 11/230875 08/17/1999 ✓  
JAPAN 11/266932 09/21/1999 ✓  
JAPAN 11/269320 09/22/1999 ✓  
JAPAN 2000-102769 04/04/2000 ✓

Projected Publication Date: Not Applicable, filed prior to November 29,2000 ✓

Non-Publication Request: No ✓

Early Publication Request: No ✓

## Title

~~Multilayer printed wiring board, solder resist composition, method for manufacturing multilayer printed wiring board, and semiconductor device.~~

## Preliminary Class

Multilayered printed circuit board, solder resist composition  
257 multilayered printed circuit board manufacturing method, and  
Semiconductor device.

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**Title 37, Code of Federal Regulations, 5.11 & 5.15**

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